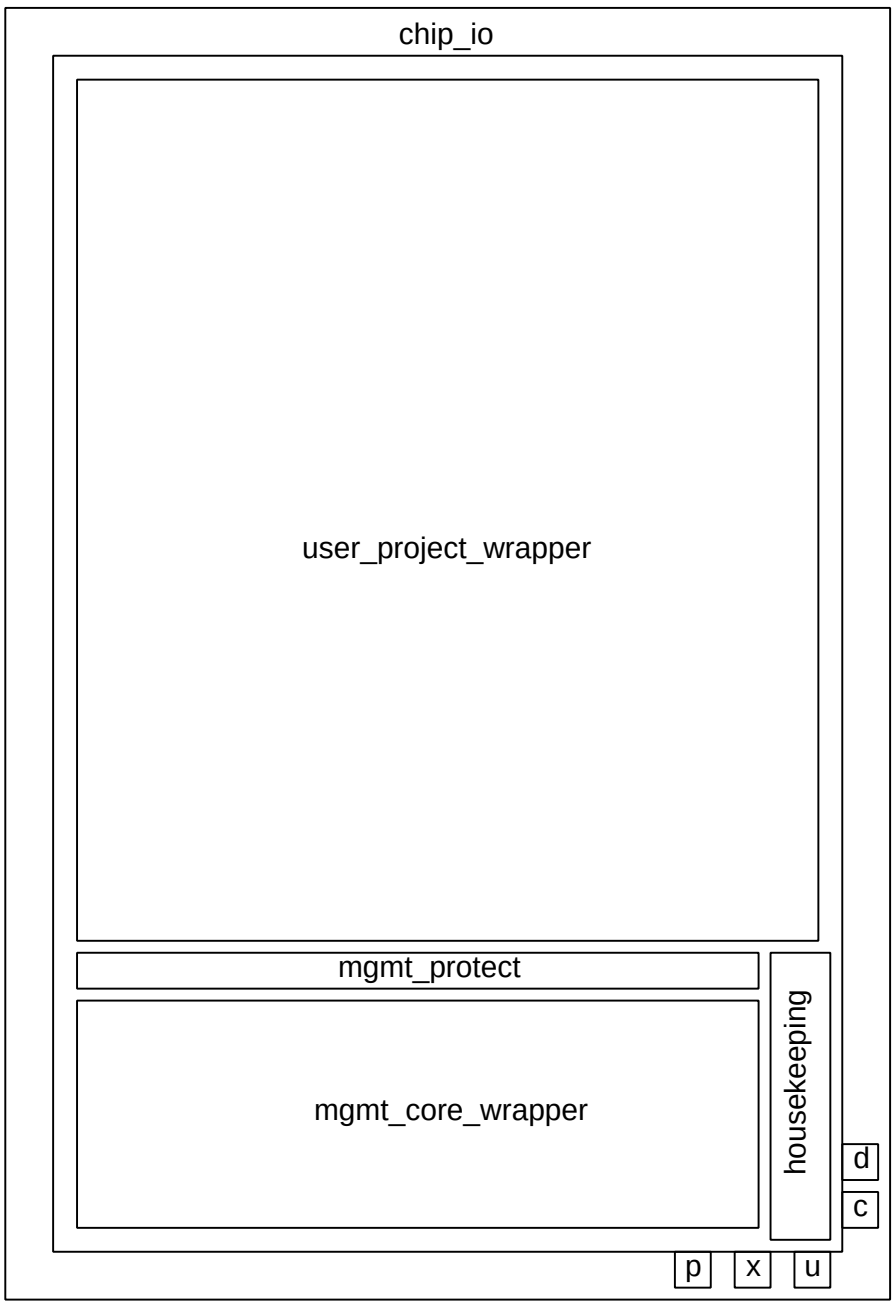

padframe:
each GPIO pad
has an associated
gpio_control_block
and
gpio_defaults_block

c= caravel_clocking

d= digital_pll

u= user_id_programming

p= simple_por

x= xres_buf