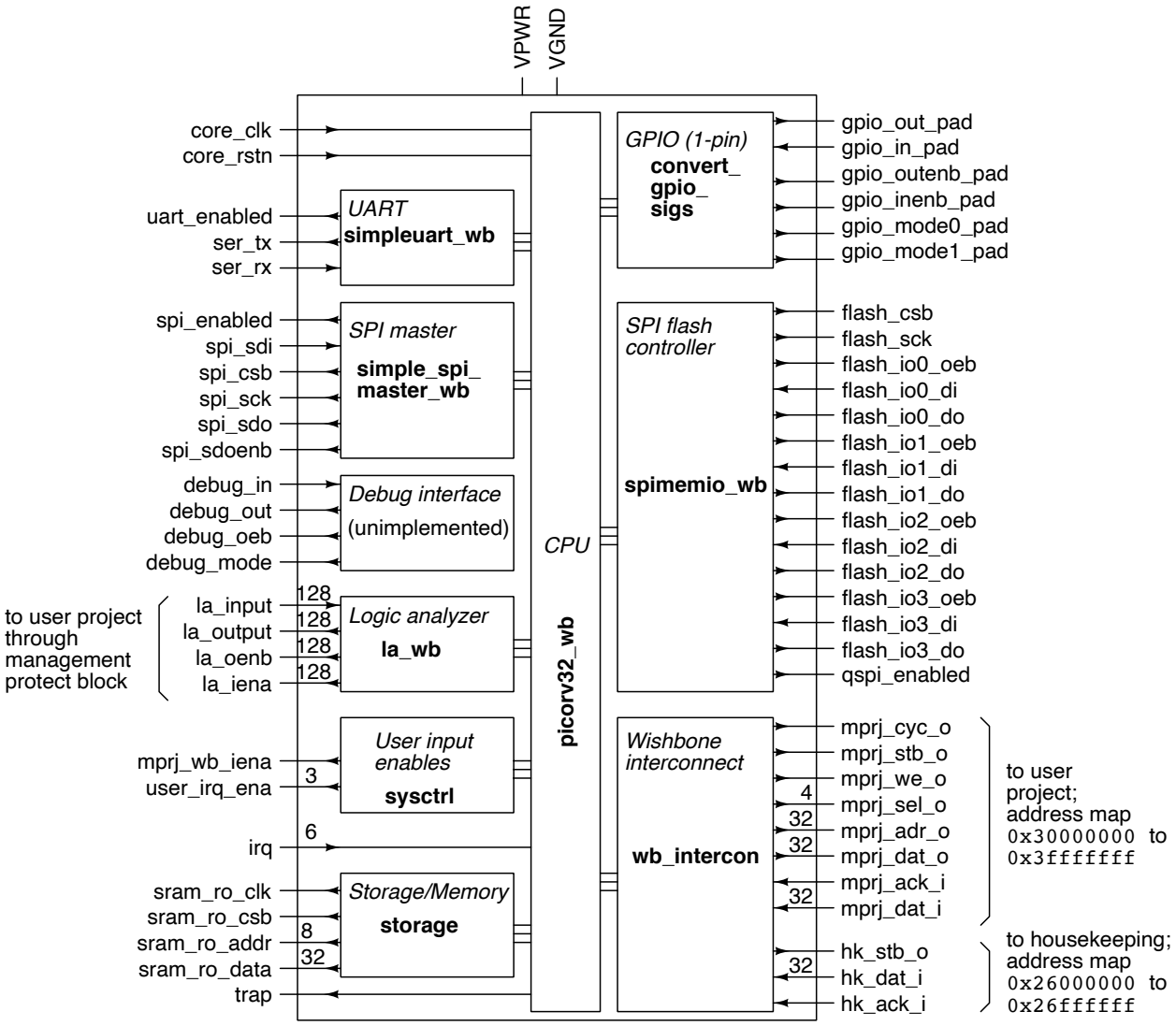
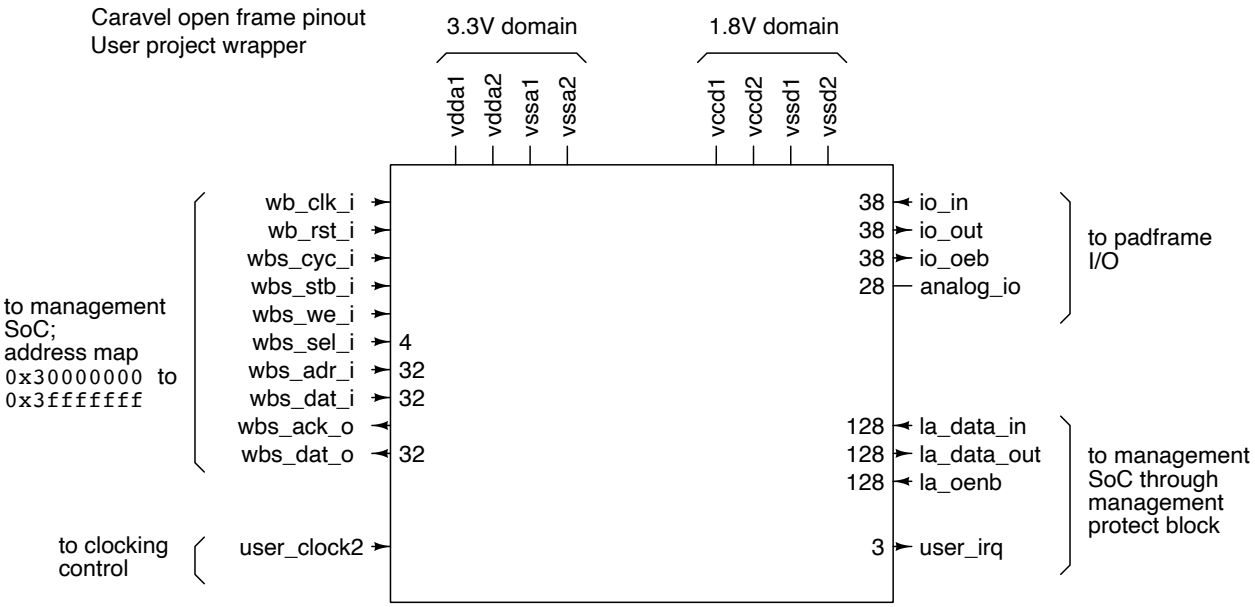
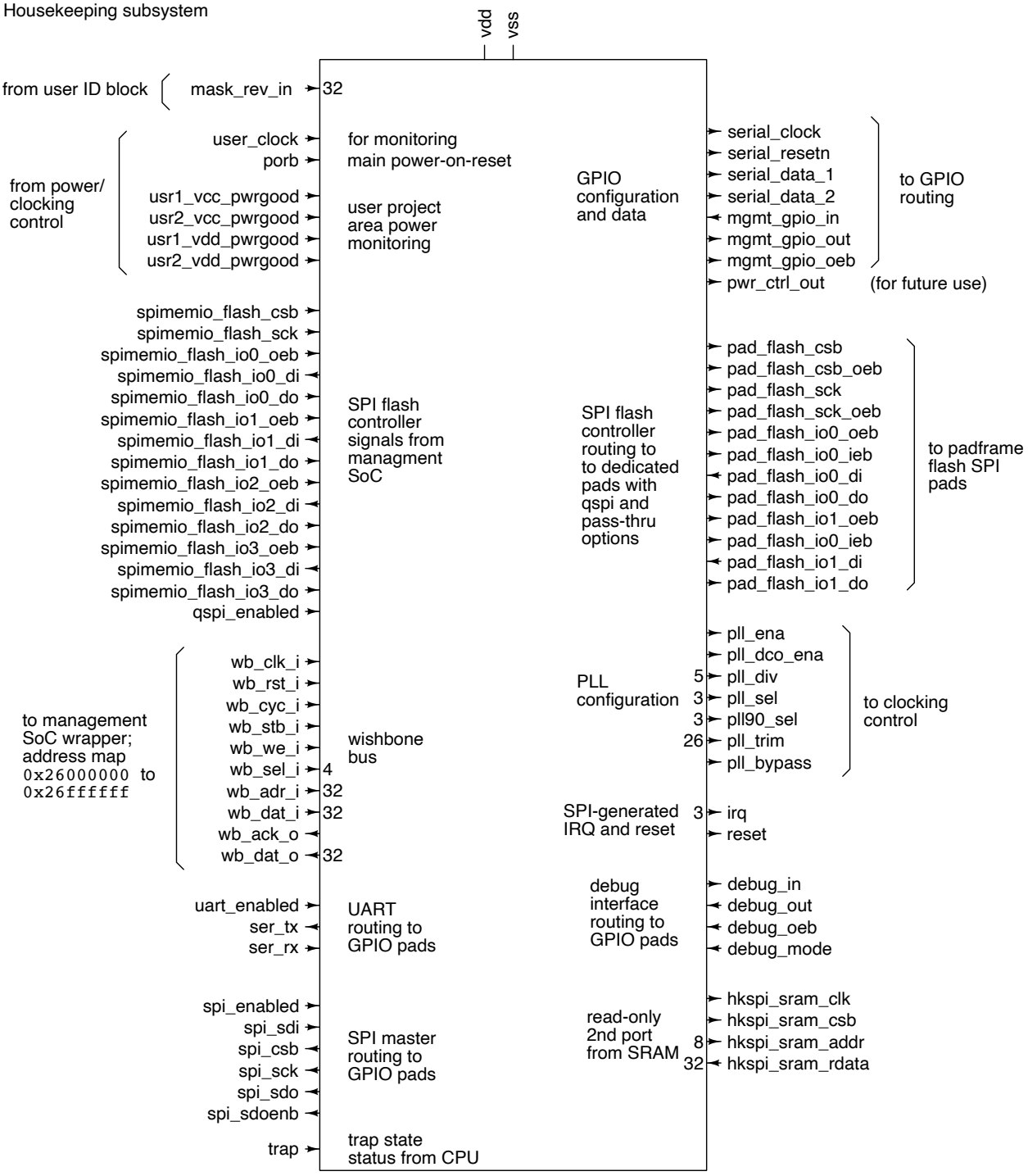
Caravel open frame pinout
Management core wrapper



All signals connect to the housekeeping subsystem unless otherwise specified.
Internal block names shown in boldface are for the **caravel-pico** implementation.


Caravel open frame pinout
User project wrapper
3.3V domain
1.8V domain
vdda1
vdda2
vssa1
vssa2
vccd1
vccd2
vssd1
vssd2
wb_clk_i
wb_rst_i
wbs_cyc_i
wbs_stb_i
wbs_we_i
wbs_sel_i 4
wbs_adr_i 32
wbs_dat_i 32
wbs_ack_o
wbs_dat_o 32
to management SoC; address map 0x30000000 to 0x3fffffff
user_clock2
to clocking control
38 io_in
38 io_out
38 io_oeb
28 analog_io
to padframe I/O
128 la_data_in
128 la_data_out
128 la_oenb
3 user_irq
to management SoC through management protect block

Caravel open frame pinout
Housekeeping subsystem



All signals connect to the management SoC wrapper unless otherwise specified.